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(58) Field of search  
G5C

## (54) LCD display devices

(57) A matrix liquid crystal display device is provided with switching transistors made of amorphous silicon thin-film for example, which are connected to each display picture element, while the switching transistor uses the scan pulse width  $H$  which is set at a value greater than that is expressed by  $T/N$ , where  $H$  denotes the scan pulse width applied to row electrodes connected to each gate of the switching transistor,  $T$  denotes the total scan time and  $N$  the number of the scan lines. Concretely, the timewise width of the scan pulse is expanded or the number of applicable scan pulses is increased (e.g. by applying scan pulses from a preceding row). The preferred embodiments relate to such a liquid crystal display driving system that can effectively minimize the decline of the voltage and the display characteristics caused by insufficient charge against the display picture element electrodes via the switching transistors. In particular, the driving method embodied by the present invention is extremely useful for driving a large-capacity X-Y matrix liquid crystal colour display device.

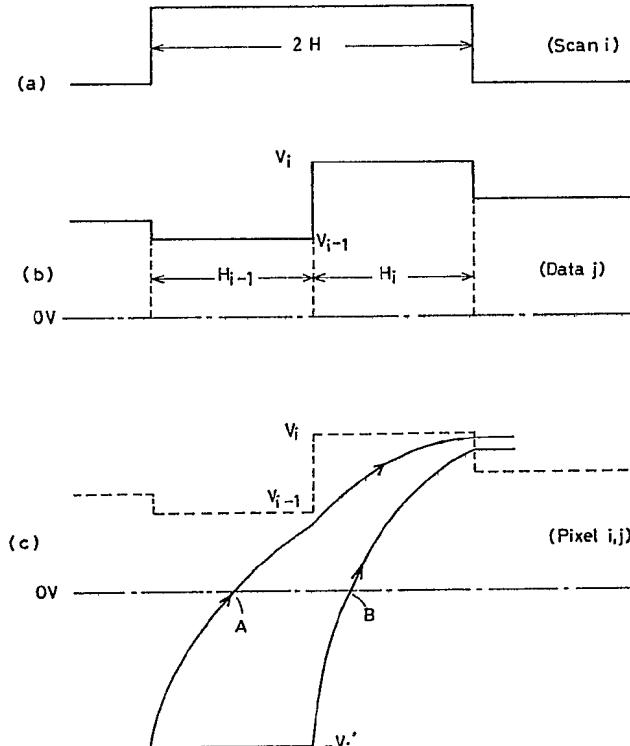


FIG. 5

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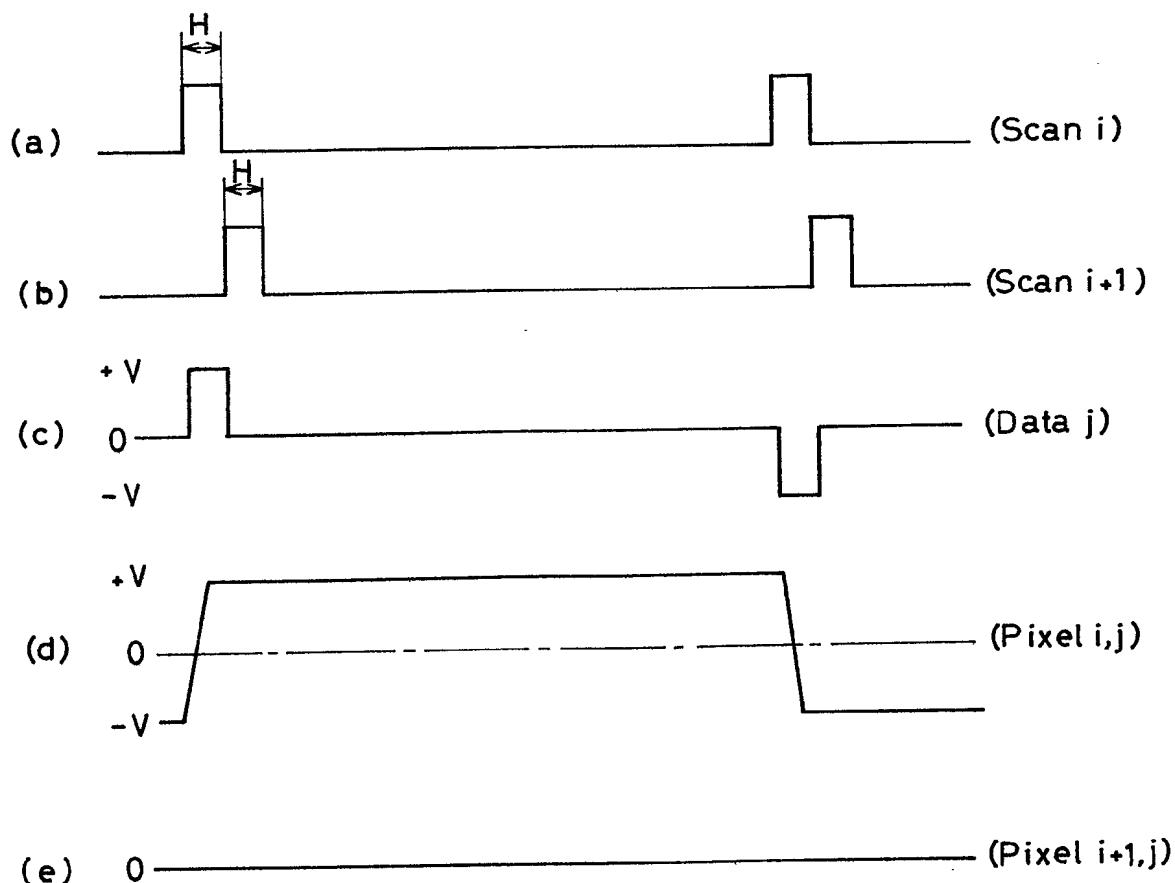
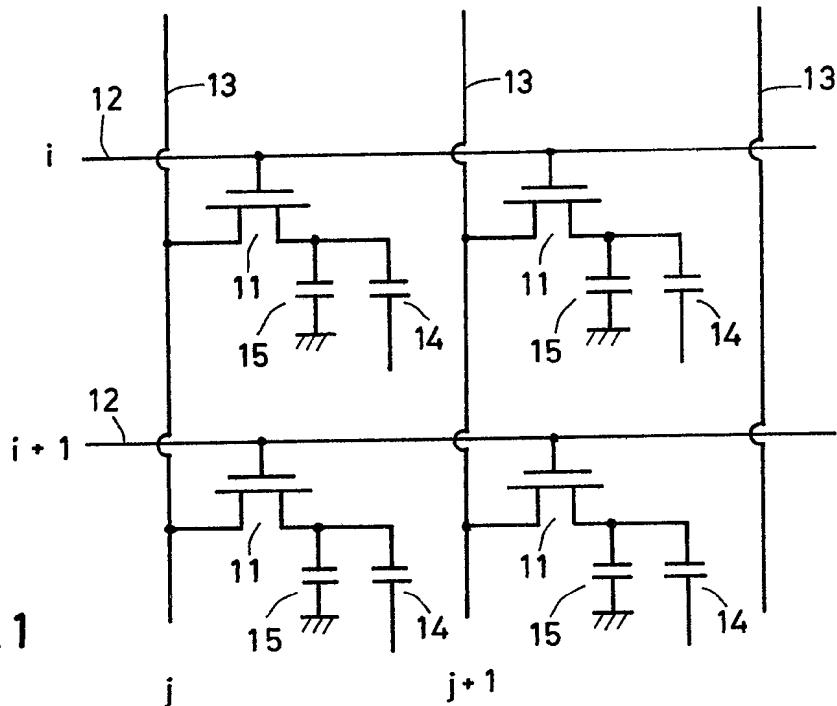


FIG.2

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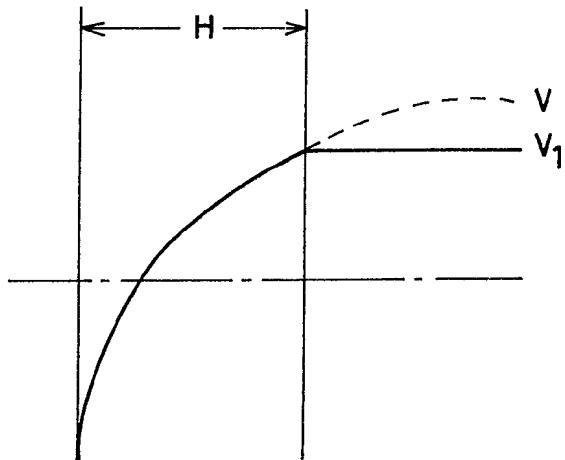


FIG. 3

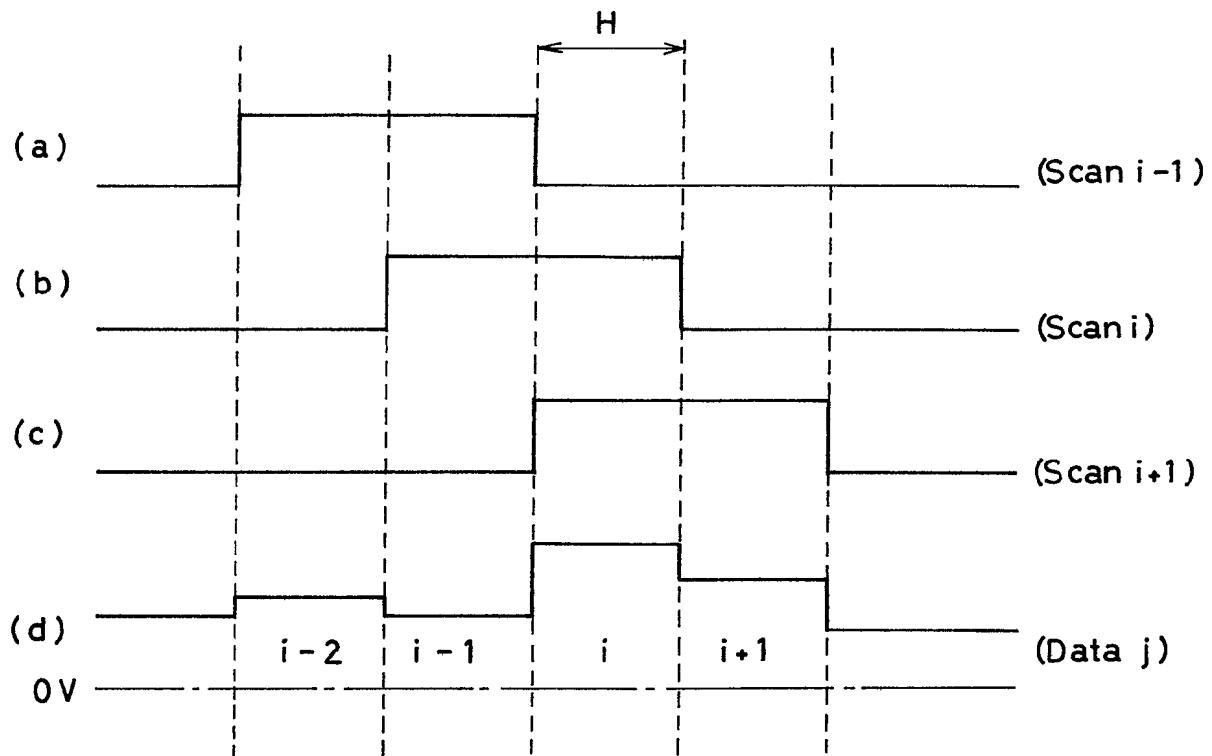


FIG. 4

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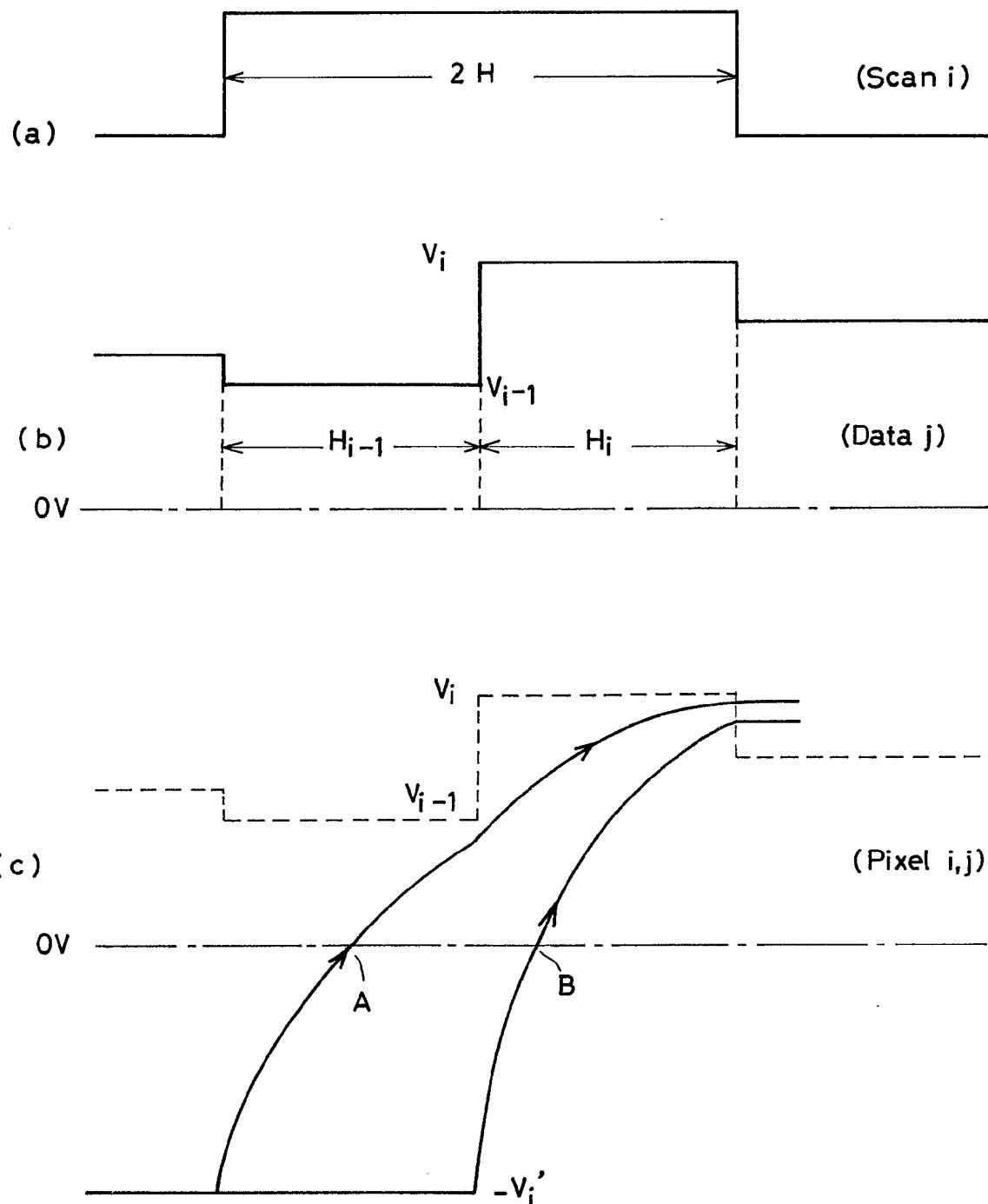


FIG.5

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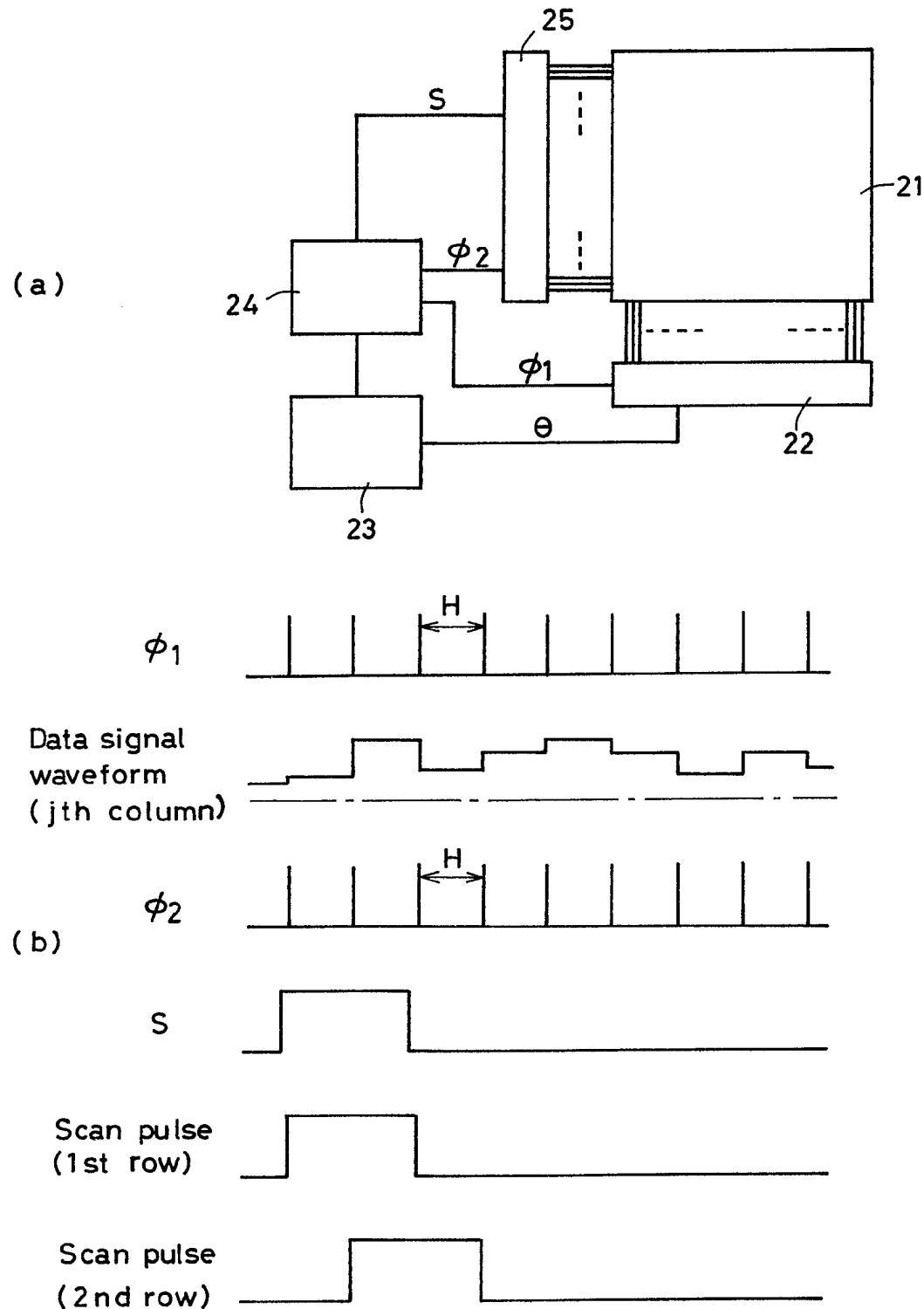


FIG.6

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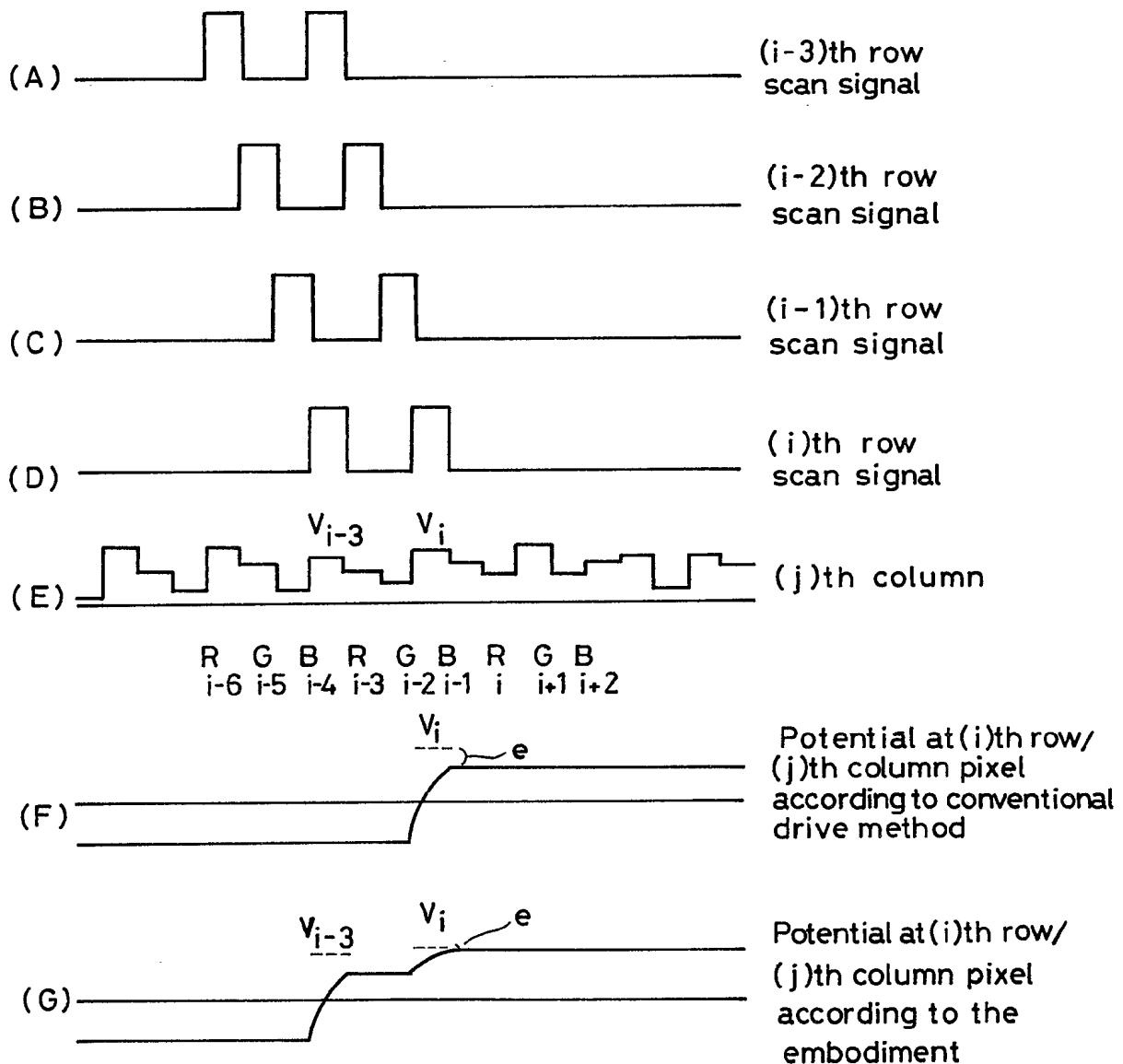
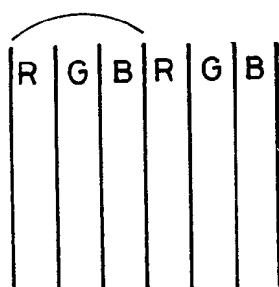
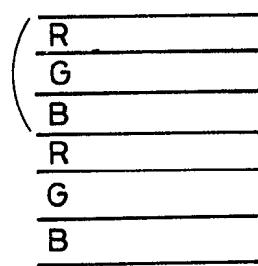


FIG.7

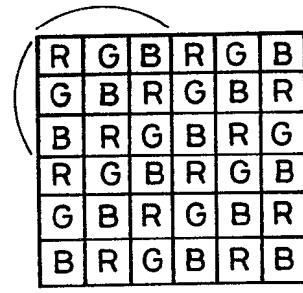
2146478



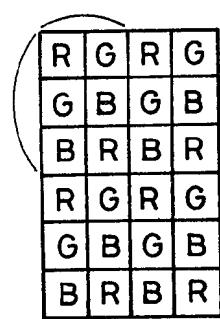
(A)



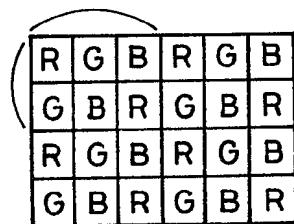
(B)



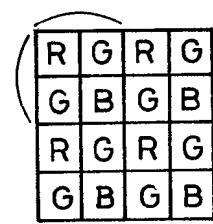
(C)



(D)



(E)



(F)

FIG.8

## SPECIFICATION

## Liquid crystal display driving with switching transistors

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*Background of the invention*

The present invention relates to a matrix liquid crystal display device, more particularly, to the method of driving the matrix liquid crystal display device provided with switching transistors for respective picture elements.

Conventionally, it is well known that, even when the small-duty or multi-line multiplex drive is performed, a high contrast display equivalent to the static-drive display can be achieved in such a matrix liquid crystal display device using switching transistors built in the LCD panel. This is typically represented by an equivalent circuit shown in Figure 1. In Figure 1, reference number 11 indicates a switching transistor, which is conventionally composed of such a field effect transistor containing semiconductive elements such as made of monocrystal, multi-crystal or amorphous silicon (Si), tellurium (Te), or cadmium selenium (CdSe) compound, etc. Reference number 12 indicates row electrodes and 13 column electrodes, which are respectively connected to the gate electrode and the source electrode of switching transistor 11. Reference number 14 indicates liquid-layer capacitors sandwiched between the display picture element electrode and the opposite electrode. Reference number 15 indicates charge storage capacitors, which are provided to compensate for insufficient charge capacitance of liquid-layer capacitors. In reference to the equivalent circuit shown in Figure 1 and the drive signal waveforms in Figure 2, functional principles of the liquid crystal display device are described below.

Figure 2 (a) and (b) respectively show scan pulses applied to the first and  $(i+1)$ th row electrodes 12. Such a pulse voltage containing the width  $H$  that turns the switching transistor 11 ON during the period  $H = T/N$  (where  $T$  denotes the total scan time and  $N$  the number of the scan line) is sequentially fed to each row electrode 12 so that each row electrode turns ON. Figure 2 (c) indicates the data signal waveforms applied to the  $j$ -th column electrode 13. Transistors in each row sequentially turn ON at the  $j$ -th position. Synchronous with it, voltage waveforms corresponding to such a voltage fed to the picture elements of respective rows are also sent to the column electrode 13. Figure 2 (c) indicates such a case in which  $V$ -volt is applied to the  $i$ -th picture element at the  $j$ -th position and 0-volt to all other picture elements. Data signal waveforms are fed in order to invert the polarity in each scan period to allow liquid crystal display to be driven by AC power.

In reference to Figure 1, picture elements of the  $i$ -th row and the  $j$ -th column are described below. When the switching transistor 11 turns ON, the column electrodes start to charge liquid crystal display and the storage capacitor 15 via resistor  $RON$  of transistor 11, causing the potential of the display picture element electrodes to become the same  $+V$

as that of data signals. When the switching transistor 11 turns OFF, the charge remains unaffected to allow the  $+V$  potential of the display picture element electrodes to also remain. When this transistor turns ON again, charge is performed so that the potential of the display picture element electrodes becomes  $-V$ , which is maintained during the following OFF period. As a result, the display picture element electrodes will receive such a voltage

70 waveform very close to the rectangular waveform shown in Figure 2 (d). Liquid crystals are driven while the potential of the opposite electrodes is zero volt with the  $V$ -value above the threshold value of liquid crystals.

75 Next, picture elements in the  $(i+1)$ th row and the  $j$ -th column are described below. In this case, the display picture element electrode is charged to become zero volt and remains charged as shown in Figure 2 (e), and as a result, no voltage is fed to liquid crystals, which then turn OFF. As described above, despite the multiplex drive thus performed, since such a stable voltage corresponding to the static driving can be supplied to liquid crystals, an extremely high-contrast display can be achieved 80 using the liquid crystal display drive embodied by the present invention.

85 In the drive method described above, it is desirable that the time constants  $RON$  and  $CL$  (where  $RON$  denotes the on-resistor of the switching transistors and  $CL$  the parallel capacitor of charge storage capacitors) be set substantially shorter than the scan pulse width  $H$  and also charge be sufficiently performed until the potential of the display picture element electrodes is equal to the voltage  $V$  90 of the data signal waveforms. This is because, if the time constants  $RON$  and  $CL$  were not less than the scan pulse width  $H$  and the display picture element electrodes were insufficiently charged, even if the voltage  $V$  is fed to the column electrodes 13,

95 liquid crystals cannot receive enough voltages, but  $V$  only, as shown in Figure 3. In addition, if such a condition exists, voltage  $V$  being fed to liquid crystals can be varied by the time constants  $RON$  and  $CL$ . As a result, if each picture element in the display unit contains varied values of the time constants  $RON$  and  $CL$ , such variations may adversely 100 affect the display contrast effect, thus eventually causing a great obstruction against such a specific display requiring interim tones as in the TV pictures.

110 In the above driving method, the scan pulse width  $H$  can be calculated by  $H = (total scan time T) / (number of scan lines N)$ . However, since  $T$  cannot be set at such a large value due to possible flicker of liquid crystals, the value  $H$  cannot be set at any value greater than a specific limit. Since it is necessary to retain a specific charge, capacitance  $CL$  cannot be reduced. Likewise, on-resistor value 115 cannot also be reduced when using such a transistor containing semiconductive elements with low conductivity. These often cause such an obstruction mentioned above.

*Object and summary of the invention*

120 In the light of various problems related to the

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conventional method of driving the matrix liquid crystal display device mentioned above, the present invention aims at providing an extremely useful method for driving the matrix liquid crystal display device, with which, a very satisfactory display contrast effect can be achieved even when the on-resistor value of the switching transistor cannot be reduced sufficiently.

The method of driving the matrix LCD device according to the preferred embodiment of the present invention features means for making the scan pulse width  $H$  greater than the value  $T/N$  (where  $T$  denotes the total scan time and  $N$  the number of scan lines), in which the scan pulse width  $H$  is applied to row electrodes which are connected to each gate of the switching transistor. The preferred embodiment makes it possible to enlarge the width of the scan pulse applied to adjacent row electrodes by generating a timewise overlap, while the waveforms generated during the drive are shown in Figure 4. An example of Figure 4 indicates such a case in which the scan pulse width is expanded twice the value of  $H$ . Figure 4 (a), (b), and (c) show the scan pulse waveforms that are respectively applied to the  $(i-1)$ th,  $i$ th, and the  $(i+1)$ th row electrodes, whereas (d) shows the data signal waveform applied to the  $j$ th column electrode. Compared to those typical waveforms generated by a conventional drive method shown in Figure 2, the drive method incorporating the preferred embodiment causes the scan pulse width to be expanded by varying the timing for turning the switching transistor OFF to ON.

In reference to the picture elements in the  $i$ -th  $j$ -th row electrode positions and also by referring to Figure 5, principles of these operations are described below. Figure 5 (a), (b), and (c) respectively show the scan pulse waveforms at the  $i$ -th row and the picture element display electrode potential at the  $j$ -th column.  $Vi-1$  and  $Vi$  respectively indicate the data voltages dealing with the  $(i-1)$ th row and the  $i$ -th row. When switching transistor 11 turns on during the  $Hi-1$  period, the display picture element electrodes in the  $i$ -th through  $j$ -th columns charge the data voltage  $Vi-1$  in the preceding row, and during the next  $Hi$  period, they also charge the data voltage  $Vi$ , thus making up the charge curve A shown in Figure 5 (c). Compared to a conventional method (see the charge curve B shown in Figure 5 (c)), the drive method based on the preferred embodiment can charge a greater voltage. When  $Vi-1$  and  $Vi$  are almost equivalent to each other as in the AC drive used for displaying TV pictures, the drive method based on the preferred embodiment expands the scan pulse width twice so that the same effect as if the time constants  $RON$  and  $CL$  were reduced to one half without the expanded scan pulse width can be achieved. When the scan pulse width is expanded, unlike the case of reducing the time constants  $RON$  and  $CL$  without expanding the pulse width, a certain difference may be generated due to any difference between  $Vi-1$  and  $Vi$ . However, if the expanded width of the scan pulse is not substantially greater than the total scan time, the differ-

ence of value is still smaller than that of liquid crystal drive voltage, thus creating no problem at all.

70 The still further preferred embodiment of the present invention related to such a matrix liquid crystal display device containing colored patterns realizes the following:

75 When the period of the colored patterns in the longitudinal direction corresponds to the picture element  $n$ , the drive method according to the still further preferred embodiment provides the same effect as if the scan pulse width were substantially expanded by preliminarily charging electrodes using the data signal of the picture elements in the row containing the same color patterns and that precedes the  $n$ -row, before actually charging electrodes with data signals. Figure 7 shows such a case where the scan pulses are applied twice to the 9-picture element pattern (Figure 8-c) containing 80 3 picture elements of the color period in the longitudinal direction. Figure 7 (a) through (d) respectively show the scan pulse waveforms applied to the  $(i-3)$ th through  $i$ -th row electrodes, whereas (e) shows the data signal waveforms applied to the 85  $j$ -th column electrode. Compared to the conventional drive waveform (f), the drive waveform (g) of the preferred embodiment substantially expands the scan pulse width by preliminarily charging electrodes with data signals fed from the same color row that precedes the  $n$ -row. In reference to 90 picture elements in the  $i$ -th row through the  $j$ -th column, principles of these operations are described below.

95 Figure 7 (g) shows the potential of the display picture element electrodes in the  $i$ -th row and the  $j$ -th column.  $Vi-n$  and  $Vi$  respectively indicate the data voltages dealing with the  $(i-n)$ th row and the  $i$ -th row. In the beginning of each field, each picture element remains charged in the reversed polarity by the preceding field. Next, when the switching transistor 11 turns ON, the display picture element electrodes in the  $i$ -th row and the  $j$ -th column start the preliminary charge against the data voltage  $Vi-n$  that precedes the  $n$ -row. The 100 switching transistor 11 then turns OFF during  $Hi-n+1$  through  $Hi-1$  periods and again turns ON during the next  $Hi$  period, thus activating charge against the data voltage  $Vi$ . As a result, such a charge curve shown in Figure 7 (g) is achieved, allowing these electrodes to charge voltages to such a level higher than the conventional drive method shown in Figure 7 (f). When the data signals  $Vi-n$  and  $Vi$  contain the same colors as in the TV picture and have a relationship close to each other, the 105 drive method according to the preferred embodiment then provides the same effect as if the time constants  $RON$  and  $CL$  were reduced to one half without expanding the scan pulse width by applying the scan pulse twice as described earlier. When the scan pulse width is expanded, unlike the case of reducing the time constants  $RON$  and  $CL$  without expanding the scan pulse width, a certain difference may be generated by any difference 110 between  $Vi-n$  and  $Vi$ . However, if  $Vi-n$  and  $Vi$  are 115 closely related and less number of rounds are 120 125 130

needed to apply the scan pulse against the total scan numbers as in the video signals, such a difference is still smaller than the liquid crystal drive voltage, thus creating no problem at all. In the 5 above preferred embodiments, use of the three primary colors' period in the longitudinal direction has been described. The drive method according to the preferred embodiment can also be applied to such cases where the two-color elements comprise 10 a color period and any other number of color elements are made available. Likewise, the pulsation number is not limited only to two rounds.

As is clear from the foregoing description, the preferred embodiments of the present invention 15 realize such a unique drive method that effectively minimizes the voltage decline caused by incomplete charge against the display picture element electrodes through the switching transistors, and yet, effectively prevents possible degradation of 20 the display characteristics. The present invention is extremely useful for stably driving the X-Y matrix liquid crystal display device featuring a large capacity.

#### 25 *Brief description of the drawings*

Figure 1 shows the equivalent circuit diagram of a matrix LCD device provided with switching transistors;

Figure 2 shows signal waveforms of electrodes 30 in the conventional drive method;

Figure 3 shows the charge curve indicating the declined voltage when the display picture element electrodes are insufficiently charged;

Figures 4 and 5 respectively show signal wave- 35 forms of respective electrodes of the drive method according to the preferred embodiment of the present invention;

Figure 6 shows the block diagram of the circuit configuration and signal waveforms according to the preferred embodiment of the present invention;

Figure 7 shows signal waveforms generated by various electrodes in the LCD drive system according to a still further preferred embodiment of the 45 present invention; and

Figure 8 shows a typical example of colored patterns.

#### *Detailed description of the embodiments*

50 Figure 6 (a) and (b) respectively show the block diagram of the drive circuit and waveforms according to the preferred embodiment of the present invention. A plurality of matrix electrodes are formed by both the row and column electrodes in the LCD 55 panel 21. Switching transistors are provided in each crossing of the matrix electrodes. Column electrodes are connected to the column electrode driver 22 composed of a shift register and a sample holder. The column electrode driver 22 outputs 60 data signal D delivered from the data controller 23 to each column electrode synchronous with the clock pulse 0 from the signal controller 24. On the other hand, row electrodes are respectively connected to the row electrode driver 25 comprising a 65 shift register, which outputs scan pulses to each

row electrode by sequentially shifting the reference scan pulse S sent from the signal controller 24 by using the clock pulse 02. At the same time, width of the scan pulse can be varied into an integer x H by varying the width of the reference scan pulse S.

Either the FET type thin-film transistor or such a thin-film transistor made of amorphous silicon formed in the cell substrate of the LCD panel 21 is made available for the switching transistor 11, in

75 which the gate electrode of the thin-film transistor is connected to the row electrodes and the source electrodes to respective column electrodes. Either the twist-nematic type liquid crystals or the guest-host effect type liquid crystals are made available, 80 while these liquid crystals receive the drive voltages via the picture element electrodes which are connected to the drain electrodes of the thin-film transistors. Clock pulses 01 and 02 from the signal controller 24 are respectively delivered to the col-

85 umn electrode driver 22 and the row electrode driver 25. The scan pulse width H provides the pulse intervals for clock pulses 01 and 02. Data signal D from the data controller 23 is delivered to the column electrode driver 22. Figure 6 (b) shows the

90 data signal D in the j-th column. Data signal D from the column electrode driver 22 is delivered to column electrodes synchronous with the clock pulse 01. The reference scan pulse S containing 2H of the pulse width out from the signal signal controller 24 is delivered to the row electrode driver

95 25. As a result, the scan pulses sent out of the row electrode driver 25 are sequentially delivered to row electrodes from the first to the last rows, as shown in Figure 6 (b). Each scan pulse containing

100 2H of the pulse width is sequentially delivered to row electrodes synchronous with the clock pulse 02 at the H-delayed intervals. This permits the scan pulses applied between adjacent row electrodes to overlap themselves during the pulse width period H. Although the preferred embodiment uses such

105 means for generating clock pulses 01 and 02 and such a circuit configuration identical to conventional means, the preferred embodiment has established its unique driving method by expanding the width of the reference scan pulse S.

110 A still further preferred embodiment causes the number of the scan pulses to vary by dividing the reference scan pulse S into a plurality of pulse arrays synchronous with the coloring period. Such

115 pulses to be delivered to row electrodes connected to each gate of the switching transistors can be composed of plural pulses containing  $(n-1) \times H$  of the intervals when the pulse width H is equal to the field time the number of row electrodes.

120 Using the color liquid crystal display panel, any desired video picture containing interim colors composed of combination colors applying the same principle as the conventional color CRT can be displayed by feeding video signals containing such colors correctly matching each picture element.

125 Also, the colored LCD display panel contains either the stripe or mosaic color filters provided against a number of picture element electrodes set in the dot-matrix form.

130 Normally, three primary colors are made avail-

able for reproducing a variety of colors. Coloring media are made of filters chosen from suitable deflecting plates, interference filters, or filters made from inorganic or organic dyestuff or pigments.

5 Such coloring media may be provided on the external or internal surface of the substrate that makes up the LCD panel. If it is provided internally, it may be set either above or below the picture element electrodes or the common electrodes. There

10 are two methods of arranging such coloring media made of colored filters, i.e., either the stripe or the mosaic formation. For example, the former includes the vertical stripe shown in Figure 8 (a), and also the horizontal stripe shown in Figure 8 (b).

15 The latter includes the 9-picture element step type shown in Figure 8 (c), the vertical 6-picture element type shown in Figure 8 (d), the horizontal 6-picture element type shown in Figure 8 (e), and the 4-picture element type shown in Figure 8 (f). In addition, there are a number of these variations made available. All of these patterns are periodic both in the vertical and horizontal directions. The period  $n$  in the vertical direction that plays a very important role in the preferred embodiments covers 1 picture

20 element in Figure 8 (a), 3 picture elements in Figure 8 (b), (c), and (d), and 2 picture elements in Figure 8 (e) and (f), respectively.

#### CLAIMS

30 1. Liquid crystal display driving system in the matrix liquid crystal display device with switching transistors connected to each display picture element comprising;

35 means for making the scan pulse width  $H$  to be greater than the value denoted by  $T/N$ , where  $H$  denotes the scan pulse width delivered to row electrodes connected to each gate of switching transistors,  $T$  denotes the total scan time and  $N$  the 40 number of scan lines.

2. Liquid crystal display driving system in the matrix liquid crystal display device with switching transistors connected to each display picture element comprising;

45 means for making  $H$  to be greater than the value denoted by  $T/N$ , where  $H$  denotes the scan pulse width delivered to row electrodes connected to each gate of switching transistors,  $T$  denotes the total scan time and  $N$  the number of scan lines, so 50 that the scan pulses applied to adjacent row electrodes can be overlapped in the timing.

3. Liquid crystal display driving system according to claim 1, which uses amorphous silicon thin-film transistors as the switching transistors.

55 4. Liquid crystal display drive system in the color picture display LCD device containing switching transistors connected to each picture element set in the matrix formation and coloring means in mosaic formation dealing with respective picture 60 elements comprising;

means for composing pulses for delivery to row electrodes connected to each gate of the switching transistors by applying a plurality of pulses each containing  $(n-1) \times H$  of intervals of the scan pulse 65 width  $H$ , where the coloring period of mosaic col-

oring means in the vertical direction covers  $n$ -picture elements and the field time the number of row electrodes =  $H$ .

70 4. A circuit for driving a capacitative display device which has a plurality of scanning electrodes and a plurality of data electrodes, the circuit being operable successively to scan the scanning electrodes while applying voltages to the data electrodes in order to produce a desired display by 75 charging a voltage across individual display elements, wherein the drive circuit is arranged to scan the scanning electrodes in such a manner that said voltage is charged throughout a period which is greater than  $T/N$ , where  $T$  denotes the total scan time and  $N$  the number of scanning electrodes.

80 5. A circuit for driving a capacitative display device which has a plurality of scanning electrodes and a plurality of data electrodes, the circuit being operable successively to scan the scanning electrodes while applying voltages to the data electrodes in order to produce a desired display by 85 charging a voltage across individual display elements, wherein the drive circuit is arranged in such a manner that charging of the voltage across a display element associated with a scanning electrode occurs in a period which overlaps that in which the voltage across a display element associated with a different scanning electrode is charged.

90 6. A liquid crystal display drive system substantially as herein described with reference to any of Figures 4 to 8 of the accompanying drawings.

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